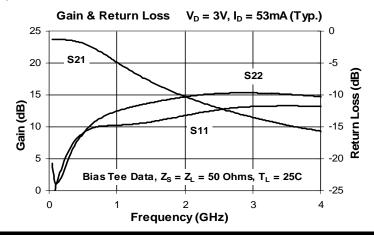


Product Description

Sirenza Microdevices' SGC-4463Z is a high performance SiGe HBT MMIC amplifier utilizing a Darlington configuration with a patented active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 3V supply, the SGC-4463Z does not require a dropping resistor as compared to typical Darlington amplifiers. The SGC-4463Z is designed for high linearity 3V gain block applications that require small size and minimal external components. It is internally matched to 50 ohms.

The matte tin finish on Sirenza's lead-free "Z" package is applied using a post annealing process to mitigate tin whisker formation and is RoHS compliant per EU Directive 2002/95. The package body is manufactured with green molding compounds that contain no antimony trioxide or halogenated fire retardants.



Preliminary Information

SGC-4463Z



50-4000 MHz Silicon Germanium Cascadable Gain Block



Product Features

- Single Fixed 3V Supply
- Supply Dropping Resistor not required
- Patented Self-Bias Circuitry
- P1dB = 13 dBm at 1950 MHz
- IP3 = 27 dBm at 1950 MHz
- Robust 1000V ESD, Class 1C HBM

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

Symbol	Parameters	Units	Frequency	Min.	Тур.	Max.
			850 MHz		21.0	
G	Small Signal Gain	dB	1950 MHz		15.0	
			2400 MHz		13.3	
			850 MHz		13.8	
P _{1dB}	Output Power at 1dB Compression	dBm	1950 MHz		13.0	Max.
			2400 MHz		12.5	
			850 MHz		28.2	
OIP ₃	Output Third Order Intercept Point	dBm	1950 MHz		27.0	
			2400 MHz			
IRL	Input Return Loss	dB	1950 MHz		13.9	
ORL	Output Return Loss	dB	1950 MHz		10.5	
NF	Noise Figure	dB	1930 MHz		3.4	
V_D	Device Operating Voltage	V			3	
I _D	Device Operating Current	mA		49	53	57
Rth, j-l	Thermal Resistance (junction to lead)	°C/W			180	

Test Conditions: $V_D = 3.0V$ $I_D = 53 \text{mA Typ.}$ $T_L = 25 ^{\circ}\text{C}$ OIP $_3$ Tone Spacing = 1MHzBias Tee Data $Z_S = Z_L = 50 \text{ Ohms}$ Pout per tone = -5 dBm

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	Typical RF Performance at Key Operating Frequencies (Bias Tee)							
Symbol	Parameter	Unit Frequency (MHz)						
	r ai airietei	Oilit	100	500	850 1950 24	2400	3500	
G	Small Signal Gain	dB	23.6	23.0	21.0	15.0	13.3	10.4
OIP ₃	Output Third Order Intercept Point	dBm		29.3	28.2	27.0	26.1	23.3
P _{1dB}	Output Power at 1dB Compression	dBm		14.4	13.8	13.0	12.5	10.6
IRL	Input Return Loss	dB	25.2	16.2	15.0	13.9	13.2	12.0
ORL	Output Return Loss	dB	24.3	16.1	13.2	10.5	9.9	10.0
S ₁₂	Reverse Isolation	dB	24.9	26.1	25.5	21.6	20.7	19.1
NF	Noise Figure	dB	2.8	2.8	3.1	3.4	3.6	4.4

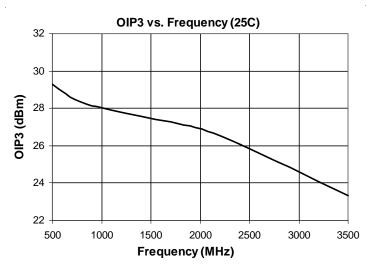
Test Conditions: $V_D = 3V$

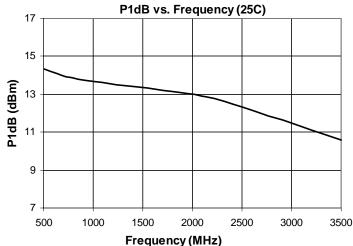
 $I_D = 53 \text{mA Typ.}$

OIP₃ Tone Spacing = 1MHz, Pout per tone = -5 dBm

 $T_L = 25$ °C $Z_S = Z_L = 50$ Ohms

Typical Performance with Bias Tee, $V_D = 3V$, $I_D = 53mA$ (Typ.)





Absolute Maximum Ratings			
Parameter	Absolute Limit		
Max Device Current (I _{CE})	110 mA		
Max Device Voltage (V _{CE})	4.5 V		
Max. RF Input Power* (See Note)	+18 dBm		
Max. Junction Temp. (T _J)	+150°C		
Operating Temp. Range (T _L)	-40°C to +85°C		
Max. Storage Temp.	+150°C		

*Note: Load condition, $Z_L = 50$ Ohms

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

Bias Conditions should also satisfy the following expression: $I_DV_D<(T_J-T_L)\:/\:R_{TH},\:j\text{--}I \qquad T_L=T_{LEAD}$

Reliability & Qualification Information	n
Parameter	Rating
ESD Rating - Human Body Model (HBM)	Class 1C
Moisture Sensitivity Level	MSL 1

This product qualification report can be downloaded at www.sirenza.com



Caution: ESD sensitive

Appropriate precautions in handling, packaging and testing devices must be observed.

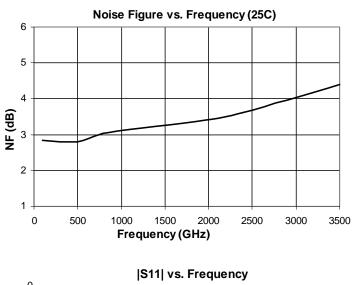
303 S. Technology Ct. Broomfield, CO 80021

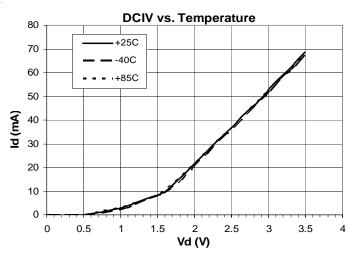
Phone: (800) SMI-MMIC

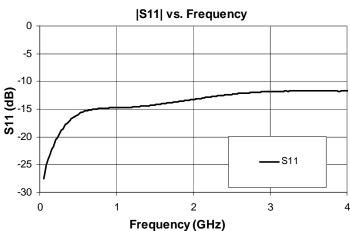
http://www.sirenza.com EDS-104979 Rev A

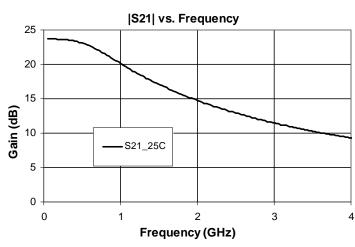


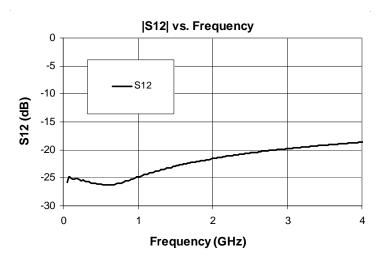
Typical Performance with Bias Tee, $V_D = 3V$, $I_D = 53mA$ (Typ.)

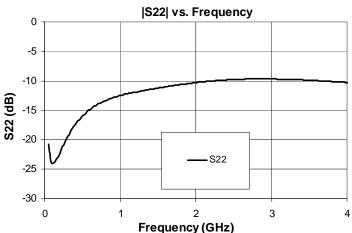






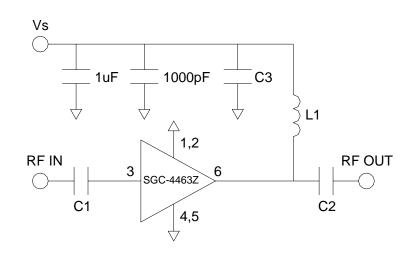


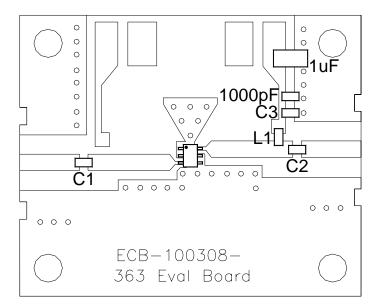




Phone: (800) SMI-MMIC 3







Pin #	Function	Description
3	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation
1,2,4,5	GND	Connection to ground. Use via holes as close to the device ground leads as possible to reduce ground inductance and achieve optimum RF performance
6	RF OUT / DCBIAS	RF output and bias pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.

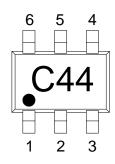
Application Circuit Schematic

Application Circuit Element Values				
Reference Designator	100-2000MHz	2000-4000MHz		
C1	1000pF	2.7pF		
C2	100pF	6.8pF		
C3	100pF	6.8pF		
L1	120nH	39nH		

Mounting Instructions

- 1. Use a large ground pad area under device pins 1, 2, 4 and 5 with many plated through-holes as shown.
- We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Part Identification Marking & Pinout



Part	Package /	Reel Size	Devices /
Number	Lead Composition		Reel
SGC-4463Z	Lead Free, RoHs Compliant	7"	3000

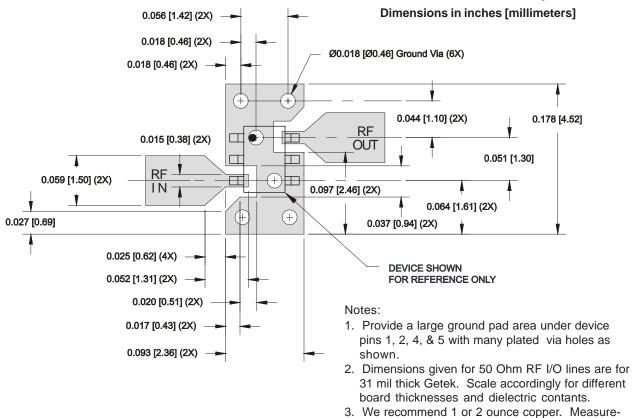
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Phone: (800) SMI-MMIC 4



SOT-363 PCB Pad Layout

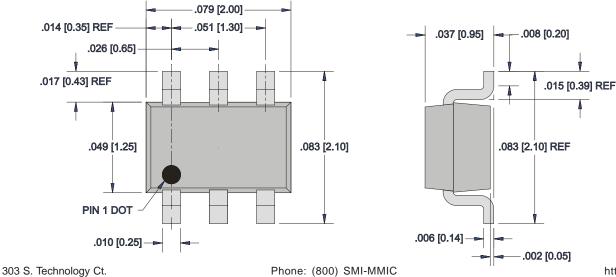
ments for this data sheet were made on a 31 mil thick Getek with 1 ounce copper on both sides.



SOT-363 Nominal Package Dimensions

Dimensions in inches [millimeters]

A link to the SOT-363 package outline drawing with full dimensions and tolerances may be found on the product web page at www.sirenza.com.



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